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Q. 6.22: For the circuit of Fig. 6.28, give three alternatives for a mod?12 counter von Dr. Dhiman Kakati vor 6 Monaten 19 Minuten 346 Aufrufe Q. 6.22: For the circuit of Fig. 6.28, give three alternatives for a mod?12 counter. (a) Using an AND gate and the load input.

Q. 3.1 Simplify following Boolean functions (a) F(x,y,z) = sum(0,2,6,7) (b) F(x,y,z) =sum(0,2,3,4,6)

Q. 3.1 Simplify following Boolean functions (a) F(x,y,z) = sum(0,2,6,7) (b) F(x,y,z) =sum(0,2,3,4,6) von Dr. Dhiman Kakati vor 1 Jahr 8 Minuten 7.834 Aufrufe Q. 3.1: Simplify the following Boolean functions, using three-variable maps: (a) F(x,y,z) = sum(0,2,, 6 , ,7) (b) F(x,y,z) = sum(0,2,3,4,, 6 ,) ...

Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the

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